



RESUME

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<u>Qualification</u>	M.E Electronics

PROFESSIONAL SUMMARY:

POST	INSTITUTION	PERIOD	
		FROM	TO
Assistant Professor	P.E.S College of Engineering, Aurangabad	28 th May, 2007	Present

UNIVERSITY APPOINTMENTS:

1. External and Internal examiner for practical/oral examination , Dr. B.A.M.U, Aurangabad

RESPONSIBILITY:

1.Sub co-ordinator in ARC Process for the year 2014 for First Year Engineering degree admission process
2. Sub co-ordinator in ARC Process for the year 2014 for Direct Second Year Engineering degree admission process.
3. Sub co-ordinator in ARC Process for the year 2015 for First Year Engineering degree admission process.
4. Sub co-ordinator in ARC Process for the year 2015 for Direct Second Year Engineering degree admission process.
5. Worked in Publicity committee formed for an event MUKT SAMWAD 2016.
6. Appointed as an invigilator for the JEE (Main)-2016 examination in April-2016.
7..Appointed as Lab Supervisor for PGAT , Maharashtra.

CONFERENCE/SEMINAR/WORKSHOP ATTENDED:

1. One day workshop attended on Satellite Communication at Institute of Satellite Telecommunication, Pune.
2. Three days Faculty Development Programme at MIT College,A'bad.

3. Paper presented in International conference on PC based security system, Nagpur.
4. Paper presented in International conference on Wireless Network: Standards and its Application, Hyderabad.
5. Two days workshop attended on Exploring the world of Electromagnetics, ICEEM A'bad.
6. Four days workshop attended on Matlab, simulink & low cost design using signal & image processing toolbox, MIT A'bad.
7. One week ISTE based workshop attended on FPGA based digital system design with application, DIEMS, A'bad.

AREAS OF INTEREST:

1. Communication Engineering
2. Digital Signal Processing