



RESUME

<u>Name</u>	Kharat Aruna P
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<u>DOB</u>	28/12/1979
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<u>Qualification</u>	M.E Electronics

PROFESSIONAL SUMMARY:

POST	INSTITUTION	PERIOD	
		FROM	TO
Assistant Professor	P.E.S College of Engineering, Aurangabad	28 th May, 2007	Present

MEMBERSHIP OF COMMITTEES:

Life member of Indian Society for Technical Education (ISTE).

UNIVERSITY APPOINTMENTS:

1. External and Internal examiner for practical/oral examination , Dr. B.A.M.U, Aurangabad

RESPONSIBILITY:

1.Sub co-ordinator in ARC Process for the year 2014 for First Year Engineering degree admission process.

2.Sub co-ordinator in ARC Process for the year 2014 for Direct Second Year Engineering degree admission process.

3. Appointed as Supervisor for PG CET (Dental)-2016, held by DMER, Maharashtra.

4. Appointed as an invigilator for the JEE (Main)-2016 examination in April-2016.

5. Worked in Anchoring committee formed for an event MUKT SAMWAD 2016.

CONFERENCE/SEMINAR/WORKSHOP ATTENDED:

1. One day workshop attended on Satellite Communication at Institute of Satellite Telecommunication, Pune.
2. Two days workshop attended on Emerging Trends in Wireless Communication, Loni.
3. Nine days Workshop attended on VLSI Technology & FACE, Pune
4. Two days workshop attended on MATLAB & Tool Boxes, Balewadi, Pune.
5. Two days workshop attended Exploring Open CV for Image & Video Processing Mit Coe Pune.
6. Two days workshop attended on Exploring the world of Electromagnetics, ICEEM A'bad.
7. One week workshop on Soft Computing ANN and Fuzzy Applications, DIEMS Aurangabad.
8. Four days workshop attended on Matlab, simulink & low cost design using signal & image processing toolbox, MIT A'bad.
9. One week ISTE based workshop attended on FPGA based digital system design with application, DIEMS, A'bad.
10. Paper Presented in National Level Conference on Mathematical Equation for Charging and Discharging of inverters Walchand Coe Sangli.
11. Paper Presented in International Conference on Evaluation of Charging and Discharging of Inverters SNJB's Coe Nasik.

EXTRA CURRICULAR ACTIVITIES

1. Conducted Educational Visit to T.V. Relay Station (HPT) at Mhaismal Aurangabad.
2. Design & developed the EDC lab. in Electronics Engineering department of P.E.S College of engineering, Aurangabad
3. Coordinator Industrial Visit to Sterlite Technologies Ltd, Waluj Midc Aurangabad.
4. Arranged Industry expert lecture for BE EC Final year students.
5. VLSI Lab development in Electronics Engineering department of P.E.S College of engineering, Aurangabad..

AREAS OF INTEREST:

1. Very Large Scale Integration.
2. Digital Signal Processing.
3. Optical and Microwave.