



RESUME

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Qualification	PhD (Electronics & Telecommunication)

PROFESSIONAL SUMMARY:

POST	INSTITUTION	PERIOD	
		FROM	TO
Lecturer /Assistant Professor/ Associate Professor in/ Professor	P.E.S College Of Engineering, Aurangabad	04/08/1997	Till Date
Test Engineer	M/s Anvi Electronics Pvt. Ltd. , Dahiser, Mumbai	02/07/1996	31/07/1997
Trainee Engineer	M/s.Sanchar Udyog Ltd. Fort , Mumbai	01/01/1996	30/06/1996

MEMBERSHIP OF COMMITTEES:

- ✓ Life member of Indian Society for Technical Education (**ISTE**).

RESPONSIBILITY:

- ✓ Head of Knowledge Center

RESEARCH ACTIVITIES:

- ✓ Ph.D. Registered as a Research Scholar in the subject Electronics & Telecommunication under the faculty of Engineering/Technology at Swami Ramanand Teerth Marathwada University, Nanded through the S.G.G.S Institute of Engineering & Technology Nanded as Research Center on the topic “**Optimization Tools for Speech Recognition**”. Research work is approved on 11/09/2013 with the ref. no. PG/Ph.D./2013-2014/1319.

CONFERENCE / SEMINAR / WORKSHOP ATTENDED STTPS -

- ✓ STTP on ‘VLSI Design’ at Dr. Babasaheb Ambedker Technological Institute, Lonere.
- ✓ One week course on ‘VLSI design’ at BIT Mapper , Pune
- ✓ Two days training program on ‘TMS320C6711 DSK & TMS320C6701 EVM’ at M/s. Cranse Software International Ltd., Bangalore.
- ✓ STTP on ‘Advance VLSI design’ at KNCOE, Pune.
- ✓ Three days training program on MATLAB & SIMULINK software’s at M/s. IndiaSoft Technologies Pvt. Ltd. Pune.
- ✓ STTP on “Recent Trends in Digital Signal & Image processing”, at SGGIE&T Nanded.
- ✓ STTP on “Microprocessor/Microcontroller Embedded Development System”, at Dynalog (India) Ltd. Pune.
- ✓ Two Days training programme on 'Effective Role Execution for Institutional Development” for Professor/Assistant Professor during February 27-28, 2009.The programme was organised by Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded in collaboration with Directorate of Technical Education (Maharashtra State) under Technical Education Quality Improvement Programme (TEQIP).

RESEARCH PAPERS PUBLISHED IN THE INTERNATIONAL JOURNALS-

- ✓ Paper on “**Field Strength Measurement of FM Signal Using DSP**”, Proceedings of National Conference on Advance in Electronics & Telecommunication Engineering 2005, COEO
- ✓ Paper on “**Multichannel Signal Processing Using TAS5508 & TAS5121**”, Proceedings of BICON-2005 at Bharti Vidyapith Pune.

- ✓ Paper on **“Performance Analysis Using MATLAB of DSP Based dB Level Meter”**, Proceedings of ELCON’05 at National Engineering College K.R.Nagar, Kovilpatti, Tamilnadu.
- ✓ Paper on **“IGBT Drive on New PWM Strategy for Wind and Other Applications “Proceeding of International conference on Wind Energy: Trends & Issues at NITTTR Bhopal.**
- ✓ Paper on **‘An Optimized Integrated Cooling, Heating and Power Generation system for buildings’** Proceedings of National Conference on Non-conventional Energy Sources and Rural Economy’ at The Institution of Engineers (India), Nagpur.
- ✓ Paper on **“Performance Analysis of Optimization Tool for Speech Recognition Using LPC & DSK TMS3206711/13 Using Simulink & Matlab”** Proceedings of International Journal of Computational Engineering Research (ijceronline.com) Vol. 2 Issue. 5
- ✓ Paper on **“Optimization Tool for Speech Recognition”**, Proceedings of IJECT Vol. 1, Issue 1, December 2010, I S S N : 2 2 3 0 - 7 1 0 9 (O n l i n e)
- ✓ Paper on **“Optimization Tool for Speech Recognition using LPC Technic & DSK TMS3206711/13”**, Proceedings of IJECT Vol. 3, Issue 3, July - Sept 2012, ISSN : 2230-7109 (Online) | ISSN : 2230-9543 (Print).
- ✓ Paper on **“Optimization of Speech Recognition using LPC Technic”**, International Organization of Scientific Research (IOSRJEN), volume 2, pp. 09-13. August 2012.
- ✓ Paper on **“Model for Optimization of Speech Recognition and Performance Analysis”**, International Journal of Electronics & Communication Technology, volume 7, issue 3, July - Sept 2016.
- ✓ Paper on **“Multichannel Signal Processing Using Tas5508 and Tas5121”**, International organization of Scientific Research, Vol. 06, Issue 07 (July 2016), ||V2|| PP 41-47

EXTRA CIRRICULAR ACTIVITIES :

- ✓ Design & develop the computer lab. & PCB design Lab. in Electronics Engineering department of P.E.S College of engineering, Aurangabad
- ✓ Participated in the testing & inspection of Electronics Voting Machine in Lokshabha & Vidhanshabha Election 2004 as per order received from Returning officer Aurangabad [west].
- ✓ Worked as the Technical Zonal Officer in Lokshabha & Vidhanshabha Election 2004 as per order received from Returning officer Aurangabad [west].
- ✓ Handling the project & Seminars Incharge ship of SE, TE, BE (EC) at P.E.S College of Engineering Aurangabad.
- ✓ Worked as Member of Paper Setting Committee for Dr. Babasaheb Ambedker Marathwada University Aurangabad.
- ✓ Examiner for the Assessment of B.E Projects for Dr. Babasaheb Ambedker Marathwada University Aurangabad.
- ✓ Examiner for the Assessment of B.E, T.E Papers for Dr. Babasaheb Ambedker Marathwada University Aurangabad.
- ✓ Given the propose syllabus for the propose subject of Applications of DSP in the first semester of final year of Engineering of Dr. Babasaheb Ambedker Marathwada University from the academic year 2005-2006, to the Electronics Engineering

Curriculum Development-Vision 2005 March 23—24, 2005, at Govt. College of Engineering, Aurangabad.

- ✓ Design & develop the new Computer laboratory including the entire layout & arrangement of computers, interfacing DSP hardware & peripherals in Electronics Engineering Department of P.E.S College of engineering, Aurangabad
- ✓ Responsible for Internet & Networking of entire computer laboratory including the VLSI laboratory, Principals Chamber, Office, Tanning & Placement Cell, Library of P.E.S College of engineering, Aurangabad.
- ✓ Recommended the students for higher studies in abroad
- ✓ Design & develop the new Knowledge Center including the entire layout & arrangement computers, wi.fi & wi-max installation in P.E.S College of Engineering, Aurangabad

AREAS OF INTEREST :

- ✓ DSP
- ✓ WIRELESS COMMUNICATION

SKILLS

- ✓ Platforms: Windows 98/2000/2003/XP, RTOS, LINUX
- ✓ Languages: C, C++, VHDL & Assembly,